

(19) World Intellectual Property
Organization
International Bureau



(43) International Publication Date
11 March 2004 (11.03.2004)

PCT

(10) International Publication Number
WO 2004/021744 A1

(51) International Patent Classification⁷: **H05B 33/08**

(21) International Application Number:
PCT/US2003/026651

(22) International Filing Date: 25 August 2003 (25.08.2003)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
10/229,602 27 August 2002 (27.08.2002) US

(71) Applicant: **FAIRCHILD SEMICONDUCTOR CORPORATION** [US/US]; 82 Running Hill Road, South Portland, ME 04106 (US).

(72) Inventors: **BURGYAN, Lajos**; 1785 Doane Avenue, Mountain View, CA 94043 (US). **PRINZ, Francois**; 232 Prague Drive, San Jose, CA 95119 (US).

(74) Agents: **WOO, Philip, W. et al.**; Sidley Austin Brown & Wood LLP, Suite 5000, 555 California Street, San Francisco, CA 94104-1715 (US).

(81) Designated States (*national*): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW.

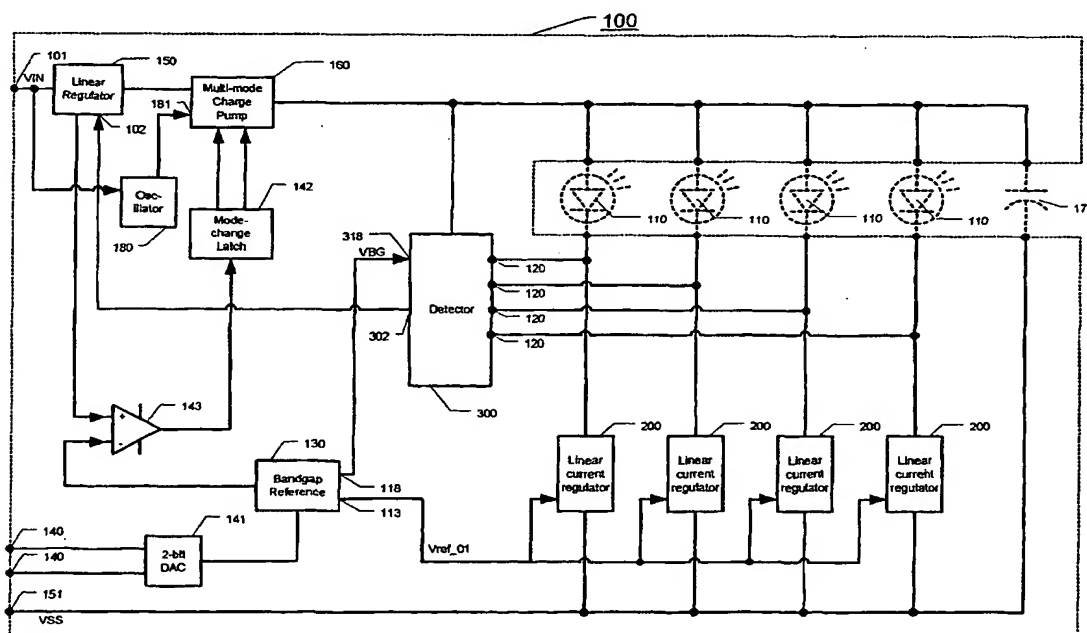
(84) Designated States (*regional*): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Declarations under Rule 4.17:

— as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD,

[Continued on next page]

(54) Title: **HIGH EFFICIENCY LED DRIVER**



(57) Abstract: Embodiments of the invention provide for circuits for driving LEDs with consistently good illumination and superior efficiency at lower cost and suitable for use with cheaper LEDs or with LEDs having wide component parameter tolerances over wide operating voltages and temperature variations. Circuits disclosed may be, but need not be, embodied on a single semiconductor chip.

WO 2004/021744 A1



MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW, ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG)

— as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii)) for all designations

Published:

— with international search report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

HIGH EFFICIENCY LED DRIVER

FIELD OF THE INVENTION

5 [0001] This invention generally relates to driver circuitry. The invention more specifically relates to a high efficiency LED (light emitting diode) driver.

BACKGROUND OF THE INVENTION

[0002] LEDs, and driver circuits to energize them, are well known. The brightness of an LED is directly related to the current applied. The voltage developed across an LED depends
10 primarily upon the semiconductor design and technology used and upon manufacturing tolerances. Where multiple LEDs are used in close proximity, it is often desirable that they operate at fairly matched light output levels. Various circuits and approaches have been previously developed for achieving uniform illumination of LEDs. But these previously developed techniques have suffered from one or more disadvantages. For example, these
15 techniques may have driver circuits operating very inefficiently. Also, the techniques may require LEDs manufactured to a close tolerance or with matched parameters established by testing. This drives up cost.

[0003] Energy efficiency is particularly important in portable devices energized by primary cells, such as the familiar alkaline "battery".

20 BRIEF DESCRIPTION OF THE DRAWINGS

[0004] For a more complete understanding of the present invention and for further features and advantages, reference is now made to the following description taken in conjunction with accompanying drawings, in which:

[0005] Fig. 1 is a circuit diagram in partial block form of an LED driver circuit according
25 to an embodiment of the invention.

[0006] Fig. 2 is a schematic diagram of a current regulator according to an embodiment of the invention.

[0007] Fig. 3 a schematic diagram of an under-voltage detector according to an embodiment of the invention.

[0008] For convenience in description, identical components have been given the same reference numbers in the various drawings.

5 DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[0009] In the following description, for purposes of clarity and conciseness of the description, not all of the numerous components shown in the schematic are described. The numerous components are shown in the drawings to provide a person of ordinary skill in the art a thorough, enabling disclosure of the present invention. The operation of many of the
10 components would be understood and apparent to one skilled in the art.

[0010] In various embodiments of the invention, circuits and methods are provided for driving LEDs with consistently good illumination and superior efficiency at lower cost and suitable for use with cheaper LEDs or with LEDs having wide component parameter tolerances over wide operating voltages and temperature variations. The circuits disclosed may be, but
15 need not be, embodied on a single semiconductor chip.

[0011] According to an aspect of the invention, a circuit is used for providing current to a plurality of LEDs which may be, but need not be, of the higher operating voltage type sometimes known as "white LEDs" for their broad optical spectrum output. The circuit may comprise a plurality of active current regulators, each controlling current in a single LED, a
20 charge pump supplying current to the plurality of LEDs, a voltage regulator supplying energy to the charge pump and a detector adapted to detect current starvation at any or all of the current limiters. According to a further aspect of the invention, the voltage regulator may have an output that is responsive to detection of current starvation.

[0012] According to a still further aspect of the invention, the charge pump may have
25 multiple operating modes, each with distinctive voltage gains. The modes may be selected according to detection of current starvation in order to provide for operation at good efficiency.

[0013] According to a further aspect of the invention, a method is provided for illuminating LEDs with relatively uniform brightness and superior overall energy efficiency as compared with previously developed methods.

[0014] Other aspects of the invention are possible, and some of them are described below.

[0015] Fig. 1 is a circuit diagram in partial block form of an exemplary LED driver circuit 100 according to a particular embodiment of the invention. LED driver circuit 100 may drive multiple LEDs 110 and, in some embodiments, may be implemented on a single integrated circuit or chip. LEDs 110 (shown in dashed outline) may be, but need not be, implemented separately from the chip for mechanical or other reasons. A smoothing capacitor 170, which may be implemented on chip or off chip, generally functions to dampen ripple.

[0016] As depicted, the LED driver circuit 100 receives a power supply input voltage VIN at an input voltage terminal 101. A linear regulator 150 is connected to input voltage terminal 101 and has a control terminal 102. Control terminal 102 receives a control input from feedback signal terminal 302 of detector 300. Linear regulator 150 may be implemented, for example, as a single MOS (metal-oxide semiconductor) transistor. Such an embodiment for the linear regulator 150 is relatively energy efficient as is desired to achieve good efficiency for the circuit 100 as a whole.

[0017] The control input to linear regulator 150 at control terminal 102 can be generated by a detector 300. The output from linear regulator 150 may be applied to a multi-mode charge pump 160. Multi-mode charge pump 160 also receives a clock signal, which can be a high frequency square wave, at input port 181. The clock signal may be generated by an oscillator 180 which may be energized from voltage VIN at terminal 101. Charge pumps to increase DC (direct current) voltages, such as by capacitor switching, are well-known in the art. Also well-known in the art are multi-mode charge pumps which can operate in any of two or more modes having distinct voltage gains according to the mode selected and voltage gain desired. In one embodiment, multi-mode charge pump 160 is capable of operating in any one of three distinct modes according to binary control signals received from a mode-change latch 142. In an embodiment of the invention, the multi-mode charge pump can operate at voltage gain ratios of 2:1 or 3:2 or as a simple passthrough at 1:1 nominal voltage gain. In practical operation, it is possible that switches in multi-mode charge pump 160 will cause some voltage drops relative to the nominal or theoretical voltage gains. Multi-mode charge pump 160 provides voltage to LEDs 110.

[0018] In one embodiment, mode-change latch 142 has a two-bit, unlocked digital output and is responsive to an analog voltage level input. A mode control OpAmp (Operational

Amplifier) comparator 143 may generate the analog signal to control the mode change latch 142.

[0019] In one embodiment, each LED 110 has an associated current regulator 200. The current passed by regulators 200 is set by reference voltage Vref_01 generated by a bandgap reference circuit 130. Bandgap reference circuits are well known in the art. In the event that the LED supply (e.g., charge pump 160) is sourcing insufficient current to an LED 110, then current starvation in the associated current regulator 200 will occur. This condition may be sensed by detector 300 as under-voltage at one or more of the input ports 120 of the detector 300. When a condition of under-voltage is detected by detector 300, detector 300 acts to control linear regulator 150 so as to increase voltage supplied to charge pump 160. This increases voltage supplied to all of the LEDs 110, thus abating the current starvation and associated under-voltage. Thus, through closed loop control, the voltage supplied to LEDs 110 is maintained at the minimum necessary to avoid current starvation. Since only the minimum voltage is maintained, current starvation will be incipient in the normal operating condition. Although some LEDs 110 may be supplied with more voltage than needed, uniform illumination is maintained because each LED 110 has its own respective current regulator 200.

[0020] The supply voltage may be insufficient for current starvation to be abated by action of the linear regulator 150 alone, as may occur for example if the power supply is a primary cell approaching the end of its useful life. In that event, control OpAmp comparator 143 detects that the linear regulator 150 is railed and signals mode change latch 142 to change the multi-mode charge pump 160 to operate in the next higher mode. This next higher mode has a higher voltage gain, and thus restores LED 110 current to a non-starving condition. Detector 300 enables linear regulator 150 to throttle back voltage supply to multi-mode charge pump 160 to the minimum necessary to restore circuit equilibrium. Thus, charge pump 160 is operated with higher gain only when necessary and hence the circuit operates with best efficiency under the prevailing supply voltage availability. Taken as a whole, the circuit design helps get maximum useful life from a primary cell power source such as may be used to energize typical portable electronic devices. In an exemplary embodiment, mode change latch 142 can act in a complementary manner to cause multi-mode charge pump 160 to operate in a more economical mode with less voltage gain if and when input voltage VIN is restored to a suitably high level.

[0021] In an exemplary embodiment, bandgap reference circuit 130 generates fixed voltage references for OpAmp comparator 143 and also a bandgap voltage VBG at reference terminal 118 applied to detector 300 at terminal 318. Bandgap reference circuit 130 may provide a bandgap voltage of approximately 1.268 volts. In the same embodiment, bandgap reference circuit 130 generates a reference voltage Vref_01 at terminal 113 to control current regulators 200. The magnitude of voltage Vref_01 may be controlled by a 2-bit binary input 140 to DAC (digital to analog converter) 141. The 2-bit binary input control functions to provide a choice of three different LED currents, and hence three different LED brightnesses, plus a dark (or off) setting of no (or negligible) LED current. The choice of brightness might be hard wired or user selectable according to product application. When the circuit 100 is in an "off" condition with the LEDs extinguished, linear regulator 150 throttles back. Nonetheless, a small current may still pass through linear regulator 150. This may serve to pre-charge capacitor 170, thus avoiding a possibly excessive startup transient.

[0022] Fig. 2 is a schematic diagram of a current regulator 200 according to an embodiment of the invention. The current regulator 200 may be connected to an LED 110. The current regulator 200 sinks current at the input terminal 211 and tends to operate as a constant current load. As such current regulator 200 is suitable for controlling current through LED 110 which may be connected to input terminal 211. Op Amp 222 operates as a voltage follower with a reference voltage provided at reference terminal 213 to control FET 220 to maintain a fixed voltage across resistor 221, and hence maintain a fixed current through resistor 221. The same fixed current flows through external LED 110. A low voltage device may be used for FET 220 so that the current regulator drops only about 700 mV for good overall circuit efficiency. In the event that the LED supply is providing insufficient current then current starvation in current regulator 200 will occur and this may be sensed externally at terminal 120 as under-voltage at input terminal 211.

[0023] Fig. 3 a schematic diagram of an under-voltage detector 300 according to an embodiment of the invention. Detector 300 receives bandgap reference voltage VBG at terminal 318. Bipolar diodes 320 operate as a wired-OR with pull-up resistor 321 to provide to OpAmp 322 a voltage at a fixed offset from the lowest of the voltages presented at the sense terminals 120. Bipolar diodes having a low threshold voltage may be used in an embodiment for wired-OR diodes 320. Resistors 352 and 354 act together with diode 358 to provide a reference voltage for OpAmp 322 that is compensated for temperature variance in the circuit.

Diodes 120 and 358 may typically be implemented in the same technology, such as bipolar, and/or on a common substrate for good temperature tracking. Op Amp 322 generates a feedback level signal at terminal 302, which is used in a closed loop control to ensure that the lowest of the voltages sensed at terminal 120 is held at the correct and near optimal value.

5 [0024] In an embodiment described above, when the circuit is operating normally, the current through each of the four LEDs 110 will be equal, but the voltages across them will depend upon individual device characteristics. Under this condition, one of the four LEDs will develop the highest voltage and correspondingly, the respective current regulator 200 will develop the minimum voltage required to sustain the desired current. This minimum voltage
10 will be the voltage sensed by the detector 300. The other LEDs 110 will develop lower voltages and their respective current regulators 200 will develop correspondingly higher voltages.

[0025] Embodiments of the invention as described herein have significant advantages over previously developed implementations. For example, an embodiment disclosed above
15 provides, as compared with previously developed solutions, a superior tradeoff between cost, uniformity and controllability of illumination intensities, energy efficiency, tolerance of wide LED operating voltages, wide power supply variations and wide component tolerances and support for multiple LEDs.

[0026] As will be apparent to one of ordinary skill in the art, other similar circuit
20 arrangements are possible within the general scope of the invention. For example, the invention need not be limited to processes providing field-effect transistors and bipolar diodes, various other types of active and non-linear devices such as JFETs (junction FETs) may be employed within the general scope of the invention. As another example, the various channels may be intentionally arranged to carry independently differing currents at the same bias
25 voltages, such independently differing currents may be fixed or adjustable also. Even embodiments with discrete components may be within the general scope of the invention. The embodiments described above are intended to be exemplary rather than limiting and the bounds of the invention should be determined from the claims.

CLAIMS

1 1. A circuit for providing current to a plurality of LEDs, the circuit comprising:
2 a plurality of current regulators, each current regulator operable to control
3 current in a respective one of the LEDs;

4 a charge pump terminal operable to supply current to the plurality of LEDs;
5 a voltage regulator operable to supply energy to the charge pump; and

6 a detector operable to detect occurrence of a condition selected from a list
7 consisting of current starvation and incipient current starvation at any of the current
8 regulators;

9 wherein the voltage regulator has an output that is responsive to detection of the
10 condition of current starvation.

1 2. The circuit of claim 1 wherein the charge pump has at least two operating
2 modes, each operating mode having a respective voltage gain, and wherein one of the at least
3 two operating modes is selected in response to detection of the condition.

1 3. The circuit of claim 1 wherein the detector comprises a wired-OR circuit
2 arrangement.

1 4. The circuit of claim 1 wherein the detector comprises bipolar diodes and the
2 plurality of current regulators comprises field effect transistors.

1 5. The circuit of claim 4 wherein the detector receives a bandgap reference voltage
2 and further comprises a temperature compensating diode and a comparator.

1 6. The circuit of claim 2 further comprising a mode latch operable to control
2 selection of an active operating mode from the at least two operating modes.

 7. The circuit of claim 6 wherein the charge pump has at least three operating
 modes.

1 8. The circuit of claim 1 wherein the current regulators carry independently
2 differing currents.

1 9. A circuit for providing current to a plurality of LEDs, the circuit comprising:

2 means for regulating current in each of the LEDs;

3 means for supplying current to the plurality of LEDs;

4 means for regulating the means for supplying current; and

5 means for detecting occurrence of a condition selected from a list consisting of
6 current starvation and incipient current starvation at the means for regulating current;

7 wherein the regulating is responsive to the detecting.

1 10. The circuit of claim 9 wherein the means for regulating comprises a charge
2 pump, the charge pump having multiple modes, each mode having a respective voltage gain.

1 11. The circuit of claim 9 wherein the means for regulating comprises a linear
2 voltage regulator.

1 12. The circuit of claim 9 wherein the means for detecting comprises a wired-OR of
2 bipolar diodes and the means for limiting current comprises a field effect transistor.

1 13. A method for providing current to a plurality of LEDs comprising:
2 regulating current in each of the LEDs to not exceed a desired amount;
3 detecting a condition selected from a list consisting of current starvation and
4 incipient current starvation in any of the LEDs; and
5 regulating a voltage supplied to the plurality of LEDs in response to the
6 detecting so that the current starvation is abated.

1 14. The method of claim 13 wherein the regulating of a voltage is performed using a
2 multi-mode charge pump.

1 15. The method of claim 14 wherein the regulating of a voltage is further performed
2 using a linear voltage regulator.

1/3

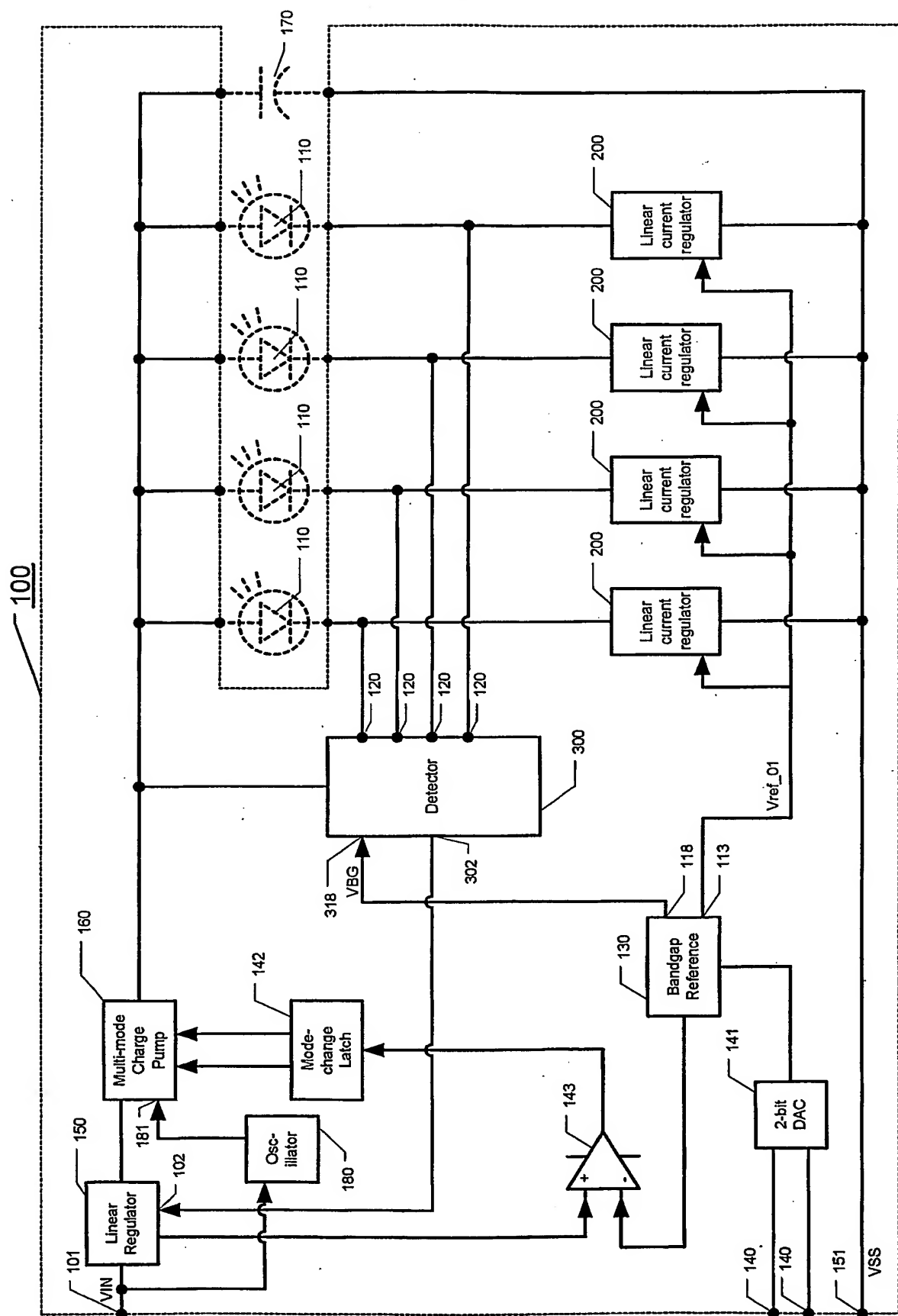


Figure 1

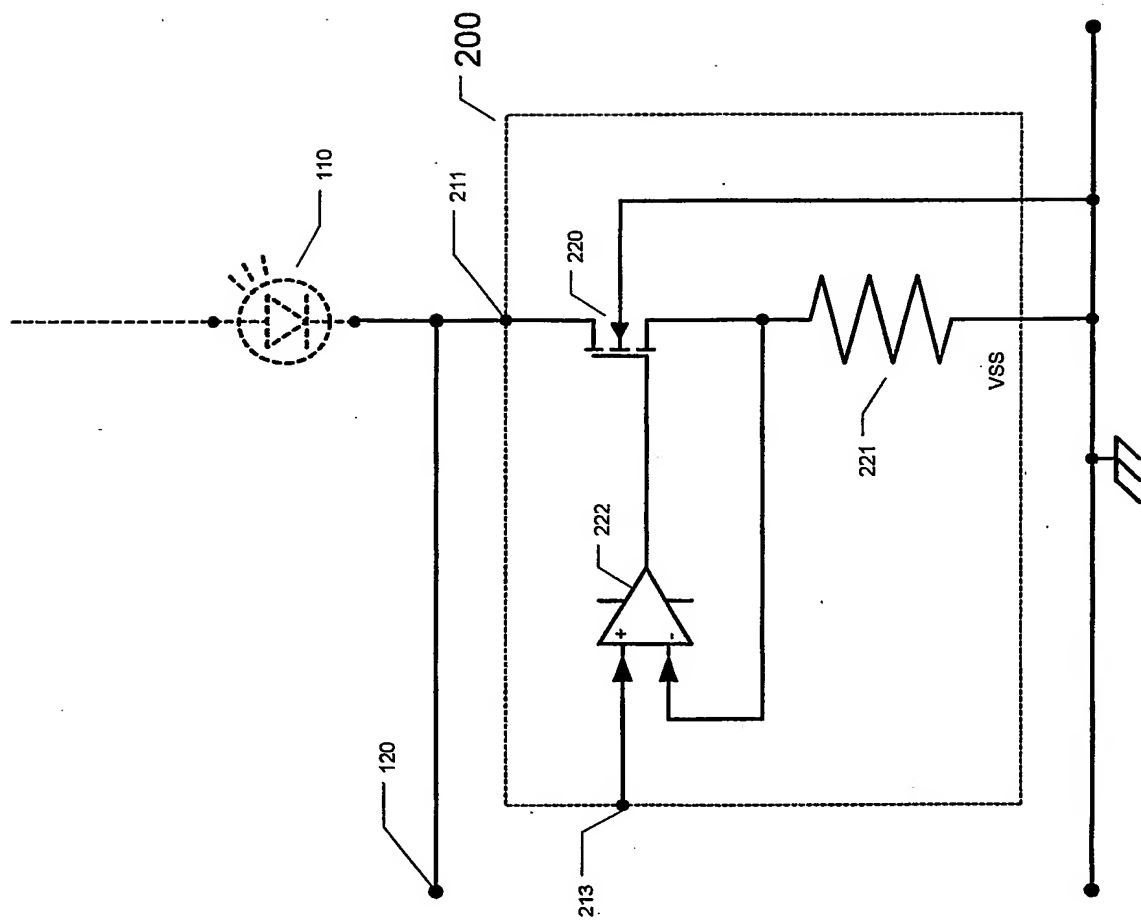


Figure 2

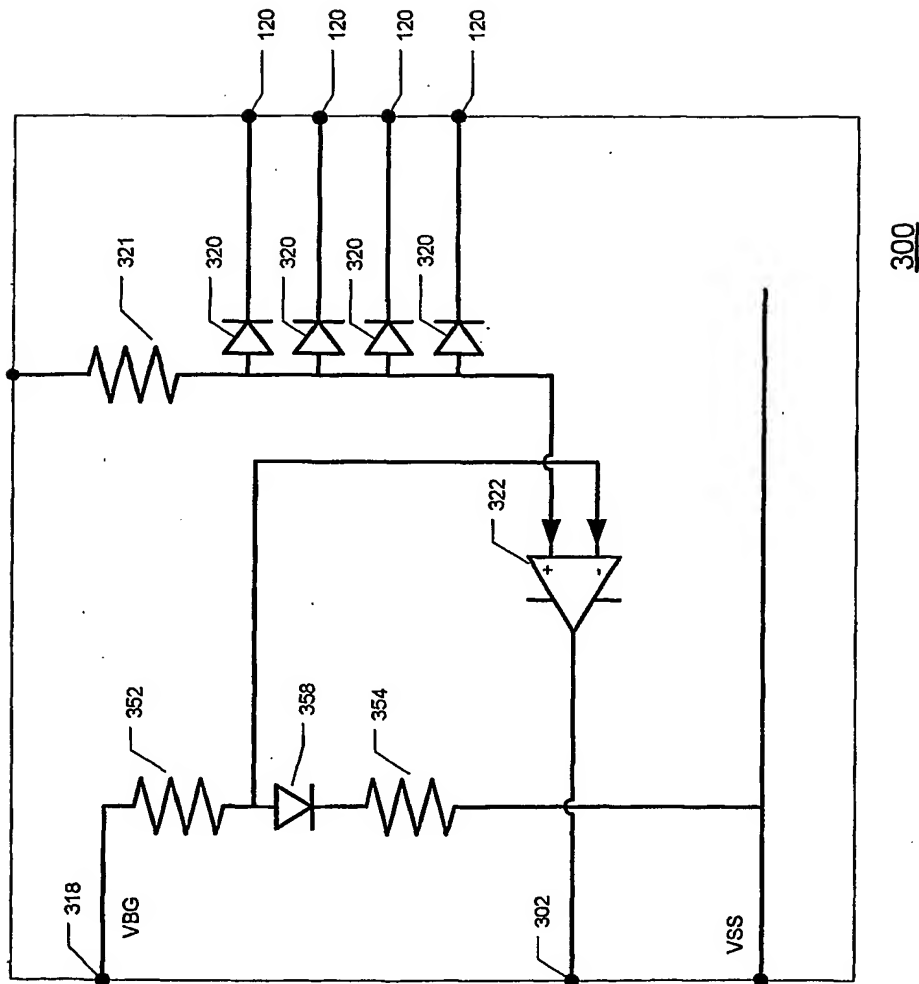


Figure 3

INTERNATIONAL SEARCH REPORT

Int l Application No
PCT/US 03/26651

A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 H05B33/08

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H05B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EP0-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	DE 100 13 216 A (TRIDONIC BAUELEMENTE) 20 September 2001 (2001-09-20) column 3, line 33 -column 4, line 29; figure 2	1-15
X	DE 100 13 215 A (TRIDONIC BAUELEMENTE) 20 September 2001 (2001-09-20) figures 1,2	9-15
X,P	EP 1 322 139 A (TOYODA GOSEI KK) 25 June 2003 (2003-06-25) column 4, line 7 -column 4, line 19 column 6, line 28 -column 7, line 12; figures 2-5	1-15
	-/-	

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

* Special categories of cited documents :

- "A" document defining the general state of the art which is not considered to be of particular relevance
- "E" earlier document but published on or after the international filing date
- "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- "O" document referring to an oral disclosure, use, exhibition or other means
- "P" document published prior to the international filing date but later than the priority date claimed

- "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
- "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
- "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.
- "&" document member of the same patent family

Date of the actual completion of the international search

1 December 2003

Date of mailing of the international search report

09/12/2003

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+31-70) 340-3016

Authorized officer

Speiser, P

INTERNATIONAL SEARCH REPORT

Int. Patent Application No.
PCT/US 03/26651

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	DE 198 41 490 A (SIEMENS AG) 23 March 2000 (2000-03-23) column 4, line 65 -column 5, line 28; figure 2 -----	9-15
A	EP 1 161 121 A (HELLA KG HUECK & CO) 5 December 2001 (2001-12-05) -----	
A	EP 1 113 708 A (ST MICROELECTRONICS INC) 4 July 2001 (2001-07-04) -----	

INTERNATIONAL SEARCH REPORT

Information on patent family members

Internat Application No
PCT/US 03/26651

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
DE 10013216	A	20-09-2001	DE 10013216 A1	20-09-2001
			EP 1185147 A1	06-03-2002
DE 10013215	A	20-09-2001	DE 10013215 A1	20-09-2001
			AU 5620701 A	24-09-2001
			WO 0169980 A1	20-09-2001
			EP 1273209 A1	08-01-2003
			US 2003117087 A1	26-06-2003
EP 1322139	A	25-06-2003	JP 2003187614 A	04-07-2003
			EP 1322139 A1	25-06-2003
			US 2003117088 A1	26-06-2003
DE 19841490	A	23-03-2000	DE 19841490 A1	23-03-2000
EP 1161121	A	05-12-2001	DE 10027478 A1	06-12-2001
			EP 1161121 A2	05-12-2001
EP 1113708	A	04-07-2001	US 6362578 B1	26-03-2002
			EP 1113708 A2	04-07-2001
			US 2003025465 A1	06-02-2003